Future Additions: USB: The Universal Serial Bus Dated: 9 January 2020

Notes and Items Planned for the Next Edition

This document is a list of subjects that I plan to add to a future edition of the book, *USB: The Universal Serial Bus*, found at http://www.fysnet.net/the_universal_serial_bus.htm

These notes are added so that you may benefit from the information now instead of waiting for the next edition. I will add information as time and interest allows.

Thank you for your support, Ben

Note added to Chapter 14:



Please note that though it is not stated within the xHCl specifications, the Extended Capabilities most likely are a set of 32-bit registers instead of a stream of 8-bit bytes. Therefore, you must read them as 32-bit reads. If you read them as bytes, the controller might return the first byte of the corresponding DWORD only. For example, if I tried to read the byte from offset 0x0501 in the dump on the previous page, the controller might return a value of 0x01 instead. Always read as DWORDs and on DWORD boundaries.

More to come...